Technology-aware multi-domain multi-layer routing

Iqbal, Farabi; van der Ham, Jeroen; Kuipers, Fernando

DOI
10.1016/j.comcom.2015.01.010

Publication date
2015

Document Version
Accepted author manuscript

Published in
Computer Communications

Citation (APA)

Important note
To cite this publication, please use the final published version (if applicable).
Please check the document version above.

Copyright
Other than for strictly personal use, it is not permitted to download, forward or distribute the text or part of it, without the consent of the author(s) and/or copyright holder(s), unless the work is under an open content license such as Creative Commons.

Takedown policy
Please contact us and provide details if you believe this document breaches copyrights.
We will remove access to the work immediately and investigate your claim.
Technology-Aware Multi-Domain Multi-Layer Routing

Farabi Iqbal\textsuperscript{a}, Jeroen van der Ham\textsuperscript{b}, Fernando Kuipers\textsuperscript{a}

\textsuperscript{a}Network Architectures and Services, Delft University of Technology, Mekelweg 4, 2628 CD Delft, The Netherlands
\textsuperscript{b}Universiteit van Amsterdam, Science Park 107, 1098 XG Amsterdam, The Netherlands

Abstract

Transporting Big Data requires high-speed connections between end-hosts. Research and educational networks typically are state-of-the-art networks that facilitate such high-speed user-created network connections, possibly spanning multiple domains. However, there are many different high-speed optical data plane standards and implementations, and vendors do not always create compatible data plane implementations. These technology incompatibilities may prevent direct communication between domains and therefore complicate the configuration of connections. However, some domains may have adaptation capabilities that can lift the technology incompatibility constraint in establishing paths between incompatible domains. Within this context, we address two problems, namely: (1) how to model the technology incompatibilities of multi-domain multi-layer networks, and (2) how to optimally establish paths in such networks. We introduce the inclusion of the information of the supported technologies and adaptation capabilities of each domain and inter-domain link in our model. We subsequently propose technology-aware routing algorithms for finding the shortest feasible path in a multi-domain multi-layer network.

Keywords: optical network, technology incompatibility, technology adaptation

1. Introduction

Many different scientific research projects are now producing Big Data. For example, the fields of physics and astronomy have traditionally been the largest producers of data with projects such as the Large Hadron Collider \cite{1}, the Sloan Digital Sky Survey \cite{2} or the planned Square Kilometer Array \cite{3} and the Large Synoptic Survey Telescope \cite{4}. We now see that other fields, such as biology and medical research, are also producing and transporting large data sets. These data sets are often shared between different institutes, within countries, but also across the globe. Most countries have their own National Research and Education Network (NREN) for providing high-speed connections between universities and research institutes within their country. For instance, the Dutch NREN is called SURFnet \cite{5}. NRENs can be considered as a catalyst of collaboration between research partners in their prospective countries. Currently, as became evident in a project with SURFnet, one of the main problems faced by NRENs is how to cooperate and pool their resources for setting up international lightpaths to fulfill the ever-increasing worldwide research needs of scientific equipment sharing, data distribution, cloud computing, etc. An example of a worldwide NREN cooperation is the Global Lambda Integrated Facility (GLIF) \cite{6} initiative.

Traditionally, NRENs are interconnected by inter-domain links between their border nodes. In the recent years, GLIF has taken the initiative to propose the use of optical exchanges as open and neutral interconnection points between NRENs, as illustrated in Figure 1. Figure 1 consists of several administrative domains, e.g., NRENs and optical exchanges, where an administrative domain is defined as a network under the control of a single network administrator. Optical exchanges, e.g., the NetherLight \cite{7} are points of presence where all NRENs that are connected to them can communicate with each other. Optical exchanges may also be connected to other optical exchanges. Ideally, the optical exchanges can adapt their client technologies transparently without any restrictions (e.g., client identities, content type or size).

Multi-domain routing is under the jurisdiction of several standardization bodies, such as the ITU Telecommunication Standardization Sector (ITU-T), the Internet Engineering Task Force (IETF), and the Open Grid Forum (OGF). Though their focus varies, all of them have proposed standards related to the multi-domain networking, namely the ITU-T G.8080/Y.1304 as...
a telecommunication standard, the Path Computation Element (PCE) framework (e.g., IETF RFC4655) as an internet standard, and the Network Service Framework (NSF) (e.g., OGF GFP173) as a grid standard.

In the ITU-T recommendation G.8080/Y.1304 [8], an architecture framework referred to as the Automatically Switched Optical Network (ASON) was proposed for a more intelligent optical network operation. The framework introduces a logical architecture of three planes, the transport plane (i.e., data plane), the control plane and the management plane. The framework also encompass the notion of domain, inter-domain links, and several routing approaches.

The IETF RFC4655 [9] aims to decouple the routing function from the control plane such that a dedicated routing component referred to as the Path Computation Element (PCE) is used instead to find more advanced paths, e.g., impairment-aware paths, multi-domain-paths, and multi-layer paths. The PCE architecture can be either centralized or distributed. Multiple PCEs work together via the use of the PCE protocol (PCEP). The standard covers inter-domain routing, intra-domain routing, and inter-layer routing. Munoz et al. [10] provided a good overview of the PCE functionality.

The OGF GFP173 [11] proposes the Network Service Interface (NSI) protocol [12] for domains to cooperate in servicing multi-domain connection requests. NSI has been implemented by various research partners of GLIF, e.g., AUTOBAHN by GÉANT, G-Lambda/A by AIST, G-Lambda/K by KDDI R&D Labs, DynamicKL by KISTI, OpenNSA by NORDUnet, OSCARS by ESnet and BoD by SURFnet [13]. Each domain is associated with a software-based management system referred to as the Network Service Agent (NSA). Multiple NSAs work collectively to establish, maintain, and terminate multi-domain connections spanning their domains. Domains are interconnected at their Service Termination Points (STPs), which represent ports on a switch, border nodes, or specific VLANs on a port as illustrated in Figure 2. A grouping of two STPs is referred to as a Service Demarcation Point (SDP). Unlike the IETF PCE framework, the OGF NSF has not yet define any specific standard for multi-domain routing.

Administrators usually build and upgrade their domain according to their preferences for vendors and technologies. These preferences could be based on capital expenditure, equipment availability, maintenance ease, etc. The wide selection of vendors and technologies leads to no de-facto standard in building domains, rendering possible technology incompatibilities between domains. Technology incompatibilities can occur in the data plane, which contains a number of switches interconnected by physical interfaces. A path between two domains is possible only if they support at least a similar technology, can adapt between the technology incompatibilities, or if there is another domain with suitable technology adaptation capability between them. Hence, routing between domains is not a trivial task. Examples of technology incompatibilities are:

- **Architecture incompatibilities** (e.g., IP over WDM [14], SONET/SDH over WDM [15], EoS over WDM [16], or Ethernet over WDM [17]) imply the needs for common lowest-layer technology and adaptation feasibility to upper layers.

- **Switching type incompatibilities** (e.g., wavelength, waveband and fibre channel at layer 1, Ethernet, Fast Distributed Data Interface (FDDI) and cell switching (ATM) at layer 2, (Generalized) Multi-Protocol Label Switching and Internet Protocol (IP) at layer 3) can exist at various layers.

- **Interface incompatibilities** (e.g., 1 GE Ethernet can be encapsulated into VC-3-21v SDH, VC-4-7v SDH, STS-1-24e SONET, or STS-3c-7v SONET) imply possible adaptation and deadaptation problems [18].

- **Rate incompatibilities** (e.g., 1, 10, 40, or 100 Gbps) imply the need for data-rate conversion.

- **Wavelength incompatibilities** (e.g., 850, 1310 or 1550 nm) imply the need for wavelength conversion.

Since the notion of technology-aware multi-domain multi-layer routing is not yet fully addressed in both IETF PCE framework and OGF NSF, and vendor interoperability issues remain an open research [19], we address this problem in this paper. First, we propose a generic network model that incorporates technology incompatibilities and scales well with the increase of graph size and number of technology incompatibilities. Our network model is applicable for use in modeling variety of technology incompatibilities that can occur in multi-domain multi-layer networks. Our network model would also be a useful addition to existing multi-domain standards, and existing technology representation approaches (e.g. NML [20]). Secondly, we propose exact and heuristic algorithms to find technology-aware loopless path from a source node to a destination node in networks with technology incompatibilities. Although triggered by a realistic problem in the NREN community, our work applies to multi-domain multi-layer networks in general.

The remainder of this paper is organized as follows. Section 2 gives an overview of related work and highlights our contributions. In Section 3, we introduce our network model and give some application examples. In Section 4, we define the problem formally, for which routing algorithms are proposed in Section 5. We present a simulative performance analysis of our algorithms in Section 6, and conclude in Section 7.
2. Related Work

In a network with limited wavelength conversion, only a subset of nodes can convert between wavelengths. A path between two distinct nodes is feasible\(^1\) if the wavelength of the path is continuous, or if appropriate wavelength conversion is conducted along the path. Chlamtac et al. [21] modeled wavelength incompatibilities by introducing a wavelength graph of \(NW\) nodes. The graph contains \(N\) columns and \(W\) rows, where \(N\) is the number of nodes in the original network, and \(W\) is the number of wavelengths. Link existence between nodes depends on the wavelength availability (horizontal links), and the wavelength conversion (vertical nodes). Though their work focuses on the intra-domain routing, their model can also be applied to multi-domain networks.

The ITU-T ASON framework does not include any specific control plane protocol, since it was meant to be a generic architectural framework. In the IETF RFC3945 [25], a control plane protocol suite referred to as the Generalized MultiProtocol Label Switching (GMPLS) [26] was proposed to support multi-layer applications that consist of different types of switching technologies. A GMPLS node may support several types of switching technologies, e.g., Packet Switch Capable (PSC), Layer 2 Switch Capable (L2SC), Time Division Multiplex Capable (TDM), Lambda Switch Capable (LSC), and Fiber Switch Capable (FSC). A connection may traverse multiple nodes with different switching technologies by the nesting of Label Switched Paths (LSPs). The order of nesting is PSC, L2SC, TDM, LSC and FSC. [22, 23, 27] have studied modeling and routing under GMPLS switching incompatibilities.

Jabbari et al. [22] proposed a channel graph of \(\sum_{e \in E} \lambda_e\) nodes, where \(E\) is the set of links and, \(\lambda_e\) is the number of switching types supported at link \(e\). Each node in the graph corresponds to a switching type supported by a link. Link existence between nodes depends on the switching capability (horizontal links), and switching adaptation (vertical links). They used a variant of Yen’s algorithm [28] as the routing algorithm. Their work was later extended in [27]. However, their solution does not consider the encapsulation order, which is important to ensure proper decapsulation. For example, if technology \(t_1\) is encapsulated in technology \(t_2\), and later in technology \(t_3\), decapsulation of \(t_3\) is required before \(t_2\) to get \(t_1\) back.

Shirazipour and Pierre [23] have used a graph of \(N\) nodes and \(E\) links as the model. Each link has a number of possible switching types, and each node has an adaptation function between the switching types. Similarly to [22], they have also used Yen’s algorithm, and later optimized the returned solution by a Binary Integer Program (BIP).

GMPLS has a limited concept of adaptation [29], which may lead to technology adaptation and deadaptation complications as highlighted by [18, 29]. Responses to GMPLS are also mixed. For example, while [22, 27] have deemed it promising, Das et al. in [30] have argued that GMPLS is completely unusable as an intelligent unified control plane for various technologies in wide-area networks. Instead of GMPLS, Lamali et al. [24] have considered the Pseudo-Wire architecture, and have proposed a language-based Push Down Automaton (PDA) model. Each protocol is represented by an alphabet, and an adaptation function between alphabets is maintained at each domain. They also developed a polynomial routing algorithm based on the PDA. Their work has been extended in [31].

Kuipers and Dijkstra [18] proposed three methods to model technology incompatibilities. Device-based, where there are \(N\) devices, with links if two devices are connected. Layer-based, where there are \(N\) devices and \(L\) technology layers. Each node corresponds to a device that is aware of its technology layer. Links are either physical links or adaptation capability between technology layers. Stack-based, of at most \(NL\) nodes, where \(N\) is the number of devices, and \(L\) is the number of technology layers. Contrary to the layer-based model, each technology incompatibility is modeled using a different layer. Hence, there are no parallel links in this model. Nodes connected by a horizontal link in a layer can communicate directly without needing

<table>
<thead>
<tr>
<th>Authors</th>
<th>Network Model</th>
<th>Encapsulation Order</th>
<th>Node Looping</th>
<th>Routing Algorithm</th>
</tr>
</thead>
<tbody>
<tr>
<td>Chlamtac et al. [21]</td>
<td>Wavelength graph</td>
<td>No</td>
<td>Yes</td>
<td>Polynomial algorithm</td>
</tr>
<tr>
<td>Jabbari et al. [22]</td>
<td>Channel graph</td>
<td>No</td>
<td>No</td>
<td>A variant of Yen’s algorithm</td>
</tr>
<tr>
<td>Kuipers and Dijkstra</td>
<td>Device-based, layer-based, and stack-based graphs</td>
<td>Yes</td>
<td>Yes (with bandwidth constraint)</td>
<td>A variant of BFS, exact algorithm and heuristic</td>
</tr>
<tr>
<td>Shirazipour and Pierre [23]</td>
<td>Simple graph with technology information</td>
<td>Yes</td>
<td>No</td>
<td>Yen’s algorithm, and optimized by a Binary Integer Program (BIP)</td>
</tr>
<tr>
<td>Lamali et al. [24]</td>
<td>Push Down Automaton (PDA)</td>
<td>Yes</td>
<td>Yes</td>
<td>Polynomial algorithm based on PDA</td>
</tr>
<tr>
<td>This paper</td>
<td>Simple graph with technology information</td>
<td>Yes</td>
<td>No</td>
<td>Exact algorithm with look-ahead function and heuristics</td>
</tr>
</tbody>
</table>

\(^1\)A feasible path faces no technology incompatibility.
3. Network Model

We propose that each domain \( n \) be characterized with a single positive additive weight \( \gamma_n \) and a binary technology (and technology encapsulation) matrix \( X_n \), while each inter-domain link \((u, v)\) is characterized by a single positive additive weight \( \ell_{uv} \) and a binary technology (and technology encapsulation) vector \( Y_{uv} \). \( \gamma_n \) can be assumed as the largest intra-domain shortest path cost between any of the STPs of the domain \( n \). \( X_n \) represents the technology (and technology encapsulations) supported by domain \( n \), while \( Y_{uv} \) represents the technologies (and technology encapsulations) supported by inter-domain link \((u, v)\). Domains can only support a finite set of technologies due to the limitations of network components. From this finite set of technologies, only some adaptations might be possible, e.g., domains can never adapt to or from unsupported technologies and the technology adaptations may or may not be reciprocal. Inter-domain links have no technology adaptation capabilities.

For example, in Figure 3, inter-domain link \((1, 3)\) with a weight of 1 supports technology 1 and 3 \((Y_{1,3}[1] = Y_{1,3}[3] = 1)\), while domain 1 with a weight of 5 supports technology 1 and 2 \((X_1[1][1] = X_1[2][2] = 1)\), and can adapt technology 1 to 2 or vice versa \((X_1[1][2] = X_1[2][1] = 1)\). One denotes that the technology is supported (or can be adapted to/from) and zero, otherwise. Domains 1 and 3 can communicate directly using technology 1. Instead of using binary values to denote technology incompatibilities, we could also use technology adaptation capabilities, we could also use technology adaptation cost as the values. In the remainder of the paper, when presenting our algorithms, we will confine to binary values.

3.1. Application Examples

We provide a generic approach to model technology incompatibilities in a multi-domain multi-layer network. Our model is applicable for various application scenarios. We proceed with two application examples of our model.

In the case of GMPLS switching incompatibilities [22, 23, 27], a domain may support one or more GMPLS switching technologies discussed in Section 2, and some domain may be able to encapsulate and decapsulate between the switching technologies, as illustrated in Figure 4. To model the network, six distinct technology representations \( t_i \) are needed:

---

\(2\) A simple path has no repeating domains.
To explain Figure 5, we take domain 1 as an example. Domain 1 supports both GE and SONET, so $X_1[1][1] = X_1[2][2] = X_1[3][3] = 1$. In this scenario, supporting SONET would imply the support of $t_2$, and $t_3$. Since domain 1 can encapsulate GE in both 21 STS and 24 STS and decapsulate it back, $X_1[1][2] = X_1[1][3] = X_1[2][1] = X_1[3][1] = 1$.

Based on the given two examples, we showed that our model is indeed generic and can be applied to model various types of technology incompatibilities. However, particular insights on the distinct technology representations are needed such that proper technology representations are considered. This can vary on a case-to-case basis. Many multi-layer and hybrid devices exist, which already solve some of the incompatibilities mentioned in this subsection. These devices are exactly what makes current multi-layer networking possible, and also so complex.

4. Problem Formulation

Problem 1. Technology-Aware Shortest Path (TASP) problem:
Consider an undirected graph $G = (N, E, T)$ consisting of a set $N$ of $N$ domains, a set $E$ of $E$ inter-domain links, and a set $T$ of $T$ incompatible technologies. Each domain $n \in N$ is characterized by a single positive additive weight $\gamma_n$ and a binary technology matrix $X_n$, while each inter-domain link $(u, v) \in E$ is characterized by a single positive additive weight $\epsilon_{uv}$ and a binary technology vector $\nu_{uv}$. Find a simple feasible path from a source domain $s$ to a destination domain $d$ such that the total path weight is minimized.

To prove that the TASP problem is NP-hard, we show that any instance of the NP-hard Min-Sum Disjoint Paths (MSDP) problem [34] can be transformed in polynomial time to an instance of the TASP problem. Though the MSDP problem was intended for intra-domain routing, we refer to it in a multi-domain context.

Problem 2. Min-Sum Disjoint Paths (MSDP):
Given a graph $G = (N, E)$, and $k$ source-destination domain pairs $(s_1, d_1), ..., (s_k, d_k)$ with each $s_i, d_i \in N$. Find $k$ disjoint paths to connect all the source-destination domain pairs with minimized total length.

Figure 6 illustrates the transformation of an instance of the MSDP problem to an instance of the TASP problem. We assume that each source-destination domain pairs $(s_i, d_i)$ from the original graph supports only technology $i$. Thus, domains $s_{2i}$ and $s_{2i+1}$ with $1 \leq i \leq \lfloor \frac{N-1}{2} \rfloor$ have incompatible technologies and cannot communicate directly. Similarly, domains $d_{2i-1}$ and $d_{2i}$ with $1 \leq i \leq \lceil \frac{N}{2} \rceil$ also have incompatible technologies and cannot communicate directly. All other domains and inter-domain links from the original graph support all technologies.

We add a new domain $x$ that supports all technologies, connecting $x$ to $s_1$ by an inter-domain link supporting technology 1. For each domain pair $(s_{2i}, s_{2i+1})$, we add an adaptation domain $a_{2i}$ that can adapt technology $2i$ to technology $2i + 1$. Each domain $a_{2i}$ is connected to $s_{2i}$ by an inter-domain link supporting technology $2i$, and to $s_{2i+1}$ by an inter-domain link supporting technology $2i + 1$. Thus, the domain pairs can now
communicate. Similarly, for each domain pair \((d_{2i-1}, d_{2i})\), we add an adaptation domain \(d_{2i-1}\) that can adapt technology \(2i-1\) to technology \(2i\). Each domain \(d_{2i-1}\) is connected to domain \(d_{2i-1}\) by an inter-domain link supporting technology \(2i-1\), and to domain \(d_{2i}\) by an inter-domain link supporting technology \(2i\). Either domain \(s_i\) or \(d_i\) with no connection to any of the new domains will be connected to a new destination domain \(y\) that supports all technologies by an inter-domain link supporting technology \(k\).

A solution to both the MSDP and TASP problems exists if a simple feasible path exists from domain \(x\) to domain \(y\). According to [34], the unweighted MSDP problem (i.e., all links have weight 1) is hard to approximate within \(\Omega(E^{1/\epsilon})\) for any constant \(\epsilon > 0\). Since a fully polynomial-time approximation scheme for the TASP problem is unlikely to exist, we focus on developing exact and heuristic algorithms.

5. Routing Algorithms

We propose Algorithm 1, which we refer to as the Exact Technology-Aware Routing Algorithm (ETARA), to solve the TASP problem. A feasible path does not succumb to technology incompatibilities. For example, although domains 1 and 3 can communicate directly using technology 1, and domains 3 and 5 can communicate directly using technologies 2 and 3 in Figure 3, \(P_1 = 1 - 3 - 5\) of weight 17 is unfeasible since domain 3 cannot adapt technologies 1 to technology 2. The actual shortest simple feasible path from domain 1 to domain 5 is \(P_2 = 1 - 2 - 3 - 5\) of weight 32. Although \(P_2\) is longer than \(P_1\), technology 1 can be adapted to technology 2 at domain 2 before proceeding to domain 5 through domain 3. Path \(P_3 = 1 - 3 - 2 - 3 - 5\) of weight 27 is ignored since it is not a simple path.

ETARA implements a \(k\)-shortest paths approach [35] by maintaining a list of feasible subpaths at each intermediate domain. To reduce the number of subpaths maintained, we prune out subpaths that use unnecessary technology adaptation. For example, path \(P_3 = 1 - 3 - 1 - 2 - 3 - 2\), which adapts technology 1 to technology 2 at domain 3 is pruned, since path \(P_3 = 1 - 3 - 2 - 3 - 2\) does not use technology adaptation at domain 2. However, if the identical subpaths use different technologies, e.g., \(P_4 = 1 - 2 - 3 - 1\) and \(P_5 = 1 - 2 - 3 - 2\), both of them will be kept since further subpath extension might need to use either of them. The subpaths of the shortest feasible path may not necessarily be shortest paths themselves. For instance, \(P_2\) uses subpath \(P_5 = 1 - 2 - 3 - 2\) with a weight of 20, which is longer than subpath \(P_4\) of weight 15. Using subpath \(P_4\) instead of \(P_5\) would however lead to a path \(P_3\) that contains loops. The shortest feasible path may also be unidirectional, and may not be simply redirected to find the shortest feasible path in the reversed direction, due to unidirectional technology adaptations of domains.

5.1. Pseudocodes

ETARA uses algorithms \(SPT\) in line 1 and \(LOOK\)–\(AHEAD\) in line 15 to reduce its search space and improving its running time. We will explain the pseudocode starting from the \(SPT\).

\(SPT\) functions to compute a shortest feasible paths tree \(Z\) rooted at the destination domain. \(SPT\) may not span all domains since it only specifies the best subpath that may lead to the shortest feasible path and ignores subpaths with higher weight. \(SPT\) first initializes the tentative weight \(D_{st}^t\) and predecessor \(\pi_{st}^t\) of all entries \(n_i\) (i.e., domain and technology pairs) in lines 1-3. For each technology supported by the destination domain, the tentative weight of the corresponding entry \(D_{st}^t\) is updated with the weight of the destination domain \(y_d\) in line 6, and inserted into the queue \(Q^t_1\) in line 7. While \(Q^t_1\) is not empty, the entry \(u_t\) with the lowest tentative weight is extracted in line 9. \(SPT\) checks if the current subpath of \(u_t\) can be extended to its adjacent domains in line 10. For each subpath extension, \(SPT\) ensures the feasibility of subpath in line 12, the subpath has no unnecessary technology adaptation in lines 15-16, and the
subpath is simple in lines 17-21. If a subpath extension is feasible, the corresponding entry $v_s$ is updated in lines 23-24, and inserted to $Q'$ in line 25.

After utilizing SPT, ETARA proceeds by initializing all the entries counter $C_{st}$ of each domain and technology pair to zero in lines 2-4. ETARA maintains $k$ subpath entries for each domain and technology pair. $k$ could grow exponentially (albeit bounded by the maximum number of possible simple paths between two domains). The optimum weight $\Delta$ is set to infinity since no feasible path has been found so far in line 5. For each technology supported by the source domain, the tentative weight of the corresponding entry $D_{stk}$ is updated with the weight of the source domain $\gamma_s$, its predecessor entry $\pi_{stk}$ set to empty in line 8, and inserted into the queue $Q$ in line 9. While $Q$ is not empty, the entry $u_t$ with the lowest tentative weight $D_{atk}$ is extracted in line 11. If $u_t$ is the destination domain, the optimum path $P$ is returned. Else, if $D_{atk}$ is lower than $\Delta$, ETARA proceeds with algorithm $\mathbf{LOOK} - \mathbf{AHEAD}$ in line 15 to tighten the value of $\Delta$.

Then, ETARA checks if the current subpath of the $u_{tk}$ can be extended to its adjacent domains in line 16. ETARA ensures the feasibility of the subpath in line 18, the subpath tentative weight is less than $\Delta$ in lines 19-20, the subpath has no unnecessary technology adaptation in lines 21-22, and the subpath is simple in lines 23-27. If a subpath extension is feasible, the corresponding entry $v_{xy}$ is updated in lines 28-31, and inserted into $Q$ in line 32.

$\mathbf{LOOK} - \mathbf{AHEAD}$ functions to tighten the value of $\Delta$ such that any subpath extension with higher tentative weight can be ignored. $\mathbf{LOOK} - \mathbf{AHEAD}$ uses the shortest feasible path tree $Z$ returned by SPT while doing so. For each extracted entry $u_{tk}$ of $Q$, $\mathbf{LOOK} - \mathbf{AHEAD}$ checks whether $u_t$ is a part of $Z$ in line 1. $\mathbf{LOOK} - \mathbf{AHEAD}$ confirms that the predecessors of the $u_{tk}$ do not coincide with any of the entries in the branch of $u_t$ in $Z$, in lines 2-9. If so, $\mathbf{LOOK} - \mathbf{AHEAD}$ tightens $\Delta$ to the weight of

---

Algorithm 1 $\mathbf{ETARA}(G, s, d)$

1: $Z \leftarrow \mathbf{spt}(G, d)$
2: for each $n \in N$
3: for each $t \in T$
4: $C_{st} \leftarrow 0$
5: $\Delta \leftarrow \infty$
6: for each $t \in T$
7: if $X_s[t][t] = 1$
8: $k \leftarrow + + C_{st}, D_{stk} \leftarrow \gamma_s, \pi_{stk} \leftarrow \text{NIL}$
9: $\text{INSERT}(Q, s_t, D_{stk}, \pi_{stk})$
10: while $Q' \neq \phi$
11: $u_t \leftarrow \text{EXTRACT-MIN}(Q')$
12: for each $v_{u_t} \in \text{adj}[u_t]$
13: $\text{GO} \leftarrow \text{FALSE}$
14: if $D_{atk} < \Delta$
15: $\mathbf{LOOK-AHEAD}(u_{tk}, \Delta, Z)$
16: for each $v_{xy} \in \text{adj}[u_{tk}]$
17: $\text{GO} \leftarrow \text{FALSE}$
18: if $X_s[t][x] = Y_s[t] = 0$
19: if $D_{stk} + \ell_{uv} + \gamma_v < \Delta$
20: $\text{GO} \leftarrow \text{TRUE}$
21: if $x \neq t$ and !(X_s[x][x] = 0 or Y_s[x][x] = 0 or $C_{ux} = 0)$
22: $\text{GO} \leftarrow \text{FALSE}$
23: $a_{bc} \leftarrow \pi_{abc}$
24: while $a_{bc} \neq \text{NIL}$ and $\text{GO} \leftarrow \text{TRUE}$
25: if $a = v$
26: $\text{GO} \leftarrow \text{FALSE}$
27: $a_{bc} \leftarrow \pi_{abc}$
28: if $\text{GO} \leftarrow \text{TRUE}$
29: $y \leftarrow + + C_{ix} + y \leq k_{ass}$ for $\text{stTARA} /$
30: $D_{xy} \leftarrow D_{stk} + \ell_{uv} + \gamma_v$
31: $\pi_{xy} \leftarrow u_{tk}$
32: $\text{INSERT}(Q, v_{xy}, D_{xy}, \pi_{xy})$

Algorithm 2 $\mathbf{spt}(G, d)$

1: for each $n \in N$
2: for each $t \in T$
3: $D'_{st} \leftarrow \infty, \pi'_{st} \leftarrow \text{NIL}$
4: for each $t \in T$
5: if $X_s[t][t] = 1$
6: $D'_{st} \leftarrow \gamma_d$
7: $\text{INSERT}(Q', s_t, D'_{st}, \pi'_{st})$
8: while $Q' \neq \phi$
9: $u_t \leftarrow \text{EXTRACT-MIN}(Q')$
10: for each $v_{u_t} \in \text{adj}[u_t]$
11: $\text{GO} \leftarrow \text{FALSE}$
12: if $X_s[t][x] = X_s[x][t] = Y_{sv}[t] = Y_{sv}[x] = 1$
13: if $D'_{st} + \ell_{uv} + \gamma_v < D'_{xv}$
14: $\text{GO} \leftarrow \text{TRUE}$
15: if $x \neq t$ and !(X_s[x][x] = 0 or Y_s[x][x] = 0)
16: $\text{GO} \leftarrow \text{FALSE}$
17: $a_{bc} \leftarrow u_{t}$
18: while $a_{bc} \neq \text{NIL}$ and $\text{GO} \leftarrow \text{TRUE}$
19: if $a = v$
20: $\text{GO} \leftarrow \text{FALSE}$
21: $a_{bc} \leftarrow \pi'_{abc}$
22: if $\text{GO} \leftarrow \text{TRUE}$
23: $D'_{xv} \leftarrow D'_{st} + \ell_{uv} + \gamma_v$
24: $\pi'_{xv} \leftarrow u_t$
25: $\text{INSERT}(Q', v_{xv}, D'_{xv}, \pi'_{xv})$

Algorithm 3 $\mathbf{LOOK-AHEAD}(u_{tk}, \Delta, Z)$

1: if $u_t \in Z$
2: $\text{GO} \leftarrow \text{TRUE}, a_{t} \leftarrow u_{t}$
3: while $\pi'_{ab} \neq \text{NIL}$ and $\text{GO} \leftarrow \text{TRUE}$
4: $e_{f} \leftarrow \pi'_{ab}, p_{ik} \leftarrow u_{ik}$
5: while $p_{ik} \neq \text{NIL}$
6: if $p = e$
7: $\text{GO} \leftarrow \text{FALSE}$
8: $p_{ik} \leftarrow \pi'_{ik}$
9: $a_t \leftarrow e_f$
10: if $D_{atk} + D'_{st} - \gamma_a < \Delta$ and $\text{GO} \leftarrow \text{TRUE} / \text{stTARA stops} /$
11: $\Delta \leftarrow D_{atk} + D'_{st} - \gamma_a$
the stitched end-to-end feasible path. Although a feasible path has been found by LOOK–AHEAD, ETARA will still proceed since this might not be the shortest feasible path from $s$ to $d$. However, if only a feasible path is required (not necessarily the shortest feasible path), one could terminate with the path as the solution. We call this variant the Feasible Technology-Aware Routing Algorithm (FTARA).

By limiting the maximum number of maintained entries $k_{\text{max}}$ for each domain and technology pair (similarly to the approach of [36] or that of a $k$-shortest path algorithm [35]), a heuristic form of ETARA, which we refer to as ($k$) Technology-Aware Routing Algorithm ($k$TARA), is obtained. $k$TARA is heuristic, because we do not know upfront how big to choose $k_{\text{max}}$ to find an exact result (while ETARA automatically adapts to the appropriate value). Hence, if we choose $k$ in $k$TARA smaller than the $k_{\text{max}}$ used by ETARA on the same instance, $k$TARA will fail to find the optimal feasible path.

5.2. Illustrative Example

Consider the problem of finding the shortest simple feasible path from domain 1 to domain 5 in the network shown in Figure 3. ETARA starts by invoking SPT. Since the destination domain 5 supports $t_1$, $t_2$, and $t_3$, the three valid entries (i.e., domain-technology pair) $5_1$, $5_2$, and $5_3$ of domain 5 will have a tentative weight of $\gamma_2 = 5$, $D'_S = D'_S = D'_S = 5$. The entries are inserted into the queue $Q$. Then, the three entries are extracted from $Q$ one by one. Since domain 5 can communicate with domain 3 using technology 1, and with domain 4 using all technologies, $D'_2$, $D'_3$, $D'_4$, and $D'_4$ are relaxed, added to $Q$ and their predecessor is set, as illustrated in Figure 7a. Then, entry $3_2$ with $D'_3 = 11$ is extracted from $Q$. Path $3_2 - 1_2$ is not feasible since inter-domain link $(2, 1)$ does not support technology 2, and path $3_2 - 5_2$ contains a loop. However, domain 3 can communicate with domain 2 using technology 2, as illustrated in Figure 7b. After that, as illustrated in Figure 7c, no further subpath extensions are feasible or optimal.

After utilizing SPT, ETARA proceeds by initializing all the valid entries (i.e., domain-technology-number-pair) of the source domain, $1_{1, 1}$ and $1_{2, 1}$, updating their tentative distance, $D_{1, 1, 1} = D_{1, 2, 1} = 5$, and inserting them into the queue $Q$ as illustrated in Figure 8a. Unlike SPT, a maximum number of $k_{\text{max}}$ entries could be maintained by ETARA for each domain-technology pair. The two entries are then extracted from $Q$ one by one while checking whether the subpath could be extended to domains 2 or 3. Since domain 1 can communicate with domain 2 using technology 1, with possible adaptation to technology 2, and with domain 3 using technology 1, entries $2_{1, 1}$, $2_{2, 1}$, and $3_{1, 1}$ are inserted into $Q$, as illustrated in Figure 8a. A remains at infinity since these entries are not in $Z$. Then, entry $3_{1, 1}$ with $D_{\text{out}} = 10$ is extracted from $Q$, which is also not part of $Z$. Path $1_{1} - 3_{1} - 5_{1}$ is not feasible due to technology restriction of inter-domain link $(3, 5)$, and paths $1_{1} - 3_{1} - 1_{1}$ and $1_{1} - 3_{1} - 1_{2}$ contain a loop. However, domain 3 can communicate with domain 2 using technology 1 and further adapt it to technology 2 as in Figure 8b. Then, entries $2_{1, 2}$ and $2_{2, 2}$ with $D_{\text{out}} = 15$ are extracted from $Q$ as illustrated in Figure 8c. Paths from domain 3 to domains 1, 2, and 4 are not feasible due to looping or technology incompatibility. Although entry $3_2$ exists in $Z$, the existence of domain 2 among its predecessors when checked by LOOK–AHEAD prevents ETARA from updating $\Delta$. When entry $3_{2, 2}$ is extracted from $Q$ in Figure 8d, Algorithm 3 confirms that the predecessors of the entry do not coincide with any of the entries in the branch of $3_{2, 2}$ in $Z$. Hence, ETARA tightens $\Delta$ to 32, the weight of the stitched feasible path $1_{1} - 2_{2} - 3_{2} - 5_{2}$. Then, entries $3_{1, 2}$ and $3_{2, 2}$ are extracted. However, all subpath extensions from them are not feasible or non-optimal. Hence, ETARA terminates and the shortest feasible path of weight 32 can be traced back from the earlier stitched path.
5.3. Time complexity

The initialization phase in lines 1-9 of ETARA has a time complexity of $O(NT \log NT + NET)$. The extract procedure in line 11 takes at most $O(k_{\text{max}}N^T \log k_{\text{max}}NT)$ time, since $Q$ contains at most $k_{\text{max}}NT$ entries. Algorithm 3 in line 15 takes at most $O(N^2)$ time. The for loop in line 16 takes at most $O(k_{\text{max}}ET)$ time, since it is invoked at most $k_{\text{max}}T$ times for each side of each inter-domain link. Lines 17-32 take at most $O(N)$ time. Summing up all the contributions, the worst-case complexity of ETARA is $O(k_{\text{max}}NT \log k_{\text{max}}NT + k_{\text{max}}NET + N^2)$. $k_{\text{max}}$ can grow exponentially with the input, implying that ETARA has an exponential running time. However, when $k_{\text{max}}$ is bounded, as in $kTARA$, the complexity is polynomial.

5.4. Correctness proof

A brute force approach would consider all possible subpath extensions from $s$ to $d$, which can be time and memory consuming. In order to make the searching process more efficient, ETARA prunes out all subpath extensions that is unfeasible, have loops, use unnecessary technology adaptations or with $D_{nk} > \Delta$. Our search-space reduction will never remove any subpath of the shortest feasible path, if it exists. If a subpath violates the technology continuity constraint or has loops, it can never be a part of the shortest feasible path and thus is safe to be ignored. If a domain could be reached directly using a technology, then it is safe to ignore any subpaths with the exact domain sequence so far that use the technology adaptation capability at the domain to adapt it to the technology. Whenever a feasible subpath is found by stitching the current subpath with the branch containing the current entry in $Z$, its $D_{nk}$ is compared with $\Delta$. If $D_{nk}$ is lower, then $\Delta$ is updated to $D_{nk}$. If $D_{nk}$ is higher, the feasible subpath could safely be ignored since another shorter subpath with the weight of $\Delta$ has already been found. We do not need to consider any extracted entry or subpath extension with $D_{nk} > \Delta$. Upon termination, ETARA will always finds the solution by retracing the shortest feasible path, if it exists. ETARA is thus guaranteed to be exact. When limiting $k_{\text{max}}$ using $kTARA$, exactness can no longer be guaranteed. If we stop once a feasible path is found using $FTARA$, the feasible path may not be the shortest feasible path.

6. Simulations

We study the performance of our algorithms in four network topologies, namely the Erdős–Rényi random network [37], the lattice network, the Waxman network [38], and the GÉANT network [39]. For the random network, the probability of an inter-domain link existence is reflected by $\frac{\log N}{N}$. The lattice network may resemble the inner core of an ultra-long-reach optical data plane systems [40]. We use a square lattice network.
of \( n \times n \) dimension where \( n = \sqrt{N} \). The Waxman network is frequently used to model communication networks [41] due to its unique property of decaying link existence over distance. In the Waxman network, the domains are uniformly positioned in the plane, and the inter-domain link existence is reflected by \( ae^{\beta p} \), where \( \beta \) is the maximum distance between any two domains in the plane, and we set \( a = 0.1 \) and \( b = 0.5 \). The GÉANT network is a realistic pan-European network interconnecting multiple countries. We refer to [42] for the GÉANT network topology.

We choose that a domain has a random weight in the range of 0.1 to 0.3, while an inter-domain link has a random weight in the range of 0.2 to 0.5 (except for the Waxman network, where the link’s weight depends on the coordinates between the endpoints). No self-loops or parallel inter-domain links are allowed. The probability that a domain or an inter-domain link supports a technology is reflected by \( p \) and \( \sqrt{p} \) respectively. The probability that a domain supports a unidirectional technology adaptation is \( p \) to the power of two, \( p^2 \), and the probability that a domain supports a bidirectional technology adaptation is \( p \) to the power of three, \( p^3 \). A domain can only adapt between its supported technologies. We compare the performance of ETARA, 1TARA, FTARA and the classical Dijkstra’s algorithm [43] in finding the shortest feasible path from a random \( s \) to a random \( d \), while varying several network characteristics. All simulation results are averaged over a hundred thousand runs. In all simulations, we terminate when the number of entries processed exceeded a million, since a solution might not exist in the randomly generated multi-domain networks.

6.1. Effect of Network Size

Figure 9 illustrates the performance of ETARA, 1TARA, FTARA, and Dijkstra’s algorithm as a function of \( N \). The optimality ratio reflects how often the algorithm was able to retrieve the shortest feasible path. Since ETARA is exact, an optimality ratio for ETARA below one indicates that in some instances, no feasible path existed. ETARA performs best in finding the optimal feasible path while 1TARA comes second due to the limitation of the number of maintained subpaths at each domain. The Dijkstra’s algorithm performs badly because the probability that the shortest path being also the shortest feasible path decreases as the network size increases. FTARA has lower optimality ratio since it terminates whenever a feasible path is found, even though the feasible path may be sub-optimal. If only a feasible path is needed, FTARA performs similarly to ETARA, as indicated by the feasibility ratio.

6.2. Effect of the Number of Technologies

Figure 10 illustrates the optimality ratio of ETARA, 1TARA, FTARA, and Dijkstra’s algorithm as a function of \( T \). As the number of technologies increases, the optimality ratio for finding the optimal path increases for both ETARA and 1TARA, since we assumed an identical probability of technology existence for all technologies. The performance of FTARA dropped back after a certain number of \( T \) is reached. When the number of possible feasible paths increases due to the increase of \( T \), the probability that FTARA terminates whenever a sub-optimal feasible path is found increases. Having higher number of technologies with similar \( p \) increases the chance of technology continuity from \( s \) to \( d \), thus increasing the performance of Dijkstra’s algorithm. Different results may be observed if each technology has a different probability of existence.

6.3. Effect of the Probability of Technology Existence

Figure 11 illustrates the optimality ratio of ETARA, 1TARA, FTARA, and Dijkstra’s algorithm as a function of the technology probability \( p \). We notice an improved performance as \( p \) increases. Vice versa, when \( p \) decreases, the multi-domain networks are more likely to break into islands of technologies, reducing the optimality ratio.

6.4. Running Time Comparison

Figure 12 plots the average running time per feasible request for ETARA, FTARA, and 1TARA. When ETARA is able to find an optimal feasible path, then FTARA also is guaranteed to find a (not necessarily feasible) path. 1TARA guarantees to only return feasible paths, but it does not guarantee to find one if it exists. Generally, the number of entries processed from \( Q \) increases as \( N \) increases because more subpaths need to be considered. With the increase of the number of subpaths due to the increase of \( N \), \( T \) or \( p \), the benefit of using Algorithm 3 increases. ETARA has highest running time, while 1TARA will becomes faster than FTARA as the network average path length becomes longer. Although FTARA can be faster than 1TARA, FTARA seldom returns the most optimal path, as shown earlier in Figure 9. An important advantage of kTARA over ETARA and FTARA is that it always terminates fast, even when no solution in the multi-domain network exists, while ETARA and FTARA may continue searching for a considerable time to come to that conclusion. Since kTARA is fast (even in absence of a feasible path) and often finds the shortest feasible path, it is our recommended algorithm for multi-domain routing with technology incompatibilities.

7. Conclusion

In this paper, we have studied the problem of finding paths in multi-domain multi-layer optical networks with technology incompatibilities. We have proposed a technology representation consisting of a technology matrix at each domain and a technology vector at each inter-domain link. In combination with costs (which could represent available bandwidth, monetary costs, impairment values, etc.) assigned to domains and inter-domain links, the technology matrices and vectors allow for flexibility in including also conversion costs, (different adaptation) policies, etc. We subsequently proposed an exact path-finding algorithm ETARA and heuristic kTARA to compute a technology-aware shortest feasible path from a source domain to a destination domain. The algorithms can be easily modified to take different objective functions (e.g., maximizing bandwidth) or QoS constraints into account. For future work, our conceptual contributions/algorithms in dealing with technology
incompatibilities could also be helpful in the Software-Defined Networking (SDN) context as well.

Acknowledgement

This research was supported by the GigaPort 2012 and 2013 Research on Networks project coordinated by SURFnet.

8. Reference

large-hadron-collider


surfnet


innovationprojects/2009/netherlight/about-netherlight/index.html

[8] “Architecture for the automatically switched optical network (ASON),”

based architecture, rfc 4655,” 2006.


